

Abstract of the Disclosure:

A method for transmitting data of a plurality of data types between a digital processor and a hardware arithmetic-logic unit with at least one associated table memory, first involves 5 preselecting a base address in the table memory that (base address) is dependent on the data type of the data to be transmitted. This is followed by a read and/or write access to the table memory by taking the preselected base address as a starting point for computing the address used for the 10 read/write access in the table memory for each access operation according to an arithmetic computation rule.

MPW/nt